# Micro/Nanolithography, MEMS, and MOEMS

Nanolithography.SPIEDigitalLibrary.org

## Investigation of defect detectability for extreme ultraviolet patterned mask using two types of highthroughput electron-beam inspection systems

Susumu Iida Ryoichi Hirano Tsuyoshi Amano Hidehiro Watanabe



### Investigation of defect detectability for extreme ultraviolet patterned mask using two types of high-throughput electron-beam inspection systems

Susumu lida,\* Ryoichi Hirano, Tsuyoshi Amano, and Hidehiro Watanabe

EUVL Infrastructure Development Center Inc., 16-1 Onogawa, Tsukuba-shi, Ibaraki-ken 305-8569, Japan

**Abstract.** Defect detectability using electron-beam (EB) inspection for an extreme ultraviolet (EUV) mask was investigated by comparing a projection electron microscope (PEM) and a scanning electron microscope (SEM) inspection system. The detectability with EB does not coincide with the printability data because the contrasts of the EUV aerial image and EB image for EUV mask are reversed. The 16-nm-sized defect on a half-pitch 64-nm line and space (L/S) pattern is detected even when the line edge roughness is taken into account in both PEM and SEM inspections by applying a special algorithm for image processing. The required and robust inspection conditions, such as the number of electrons per pixel and pixel size (resolution), were examined for an SEM inspection system. The throughput of the PEM inspection system corresponds to that of the multibeam SEM one with 200 to 1850 beams. © The Authors. Published by SPIE under a Creative Commons Attribution 3.0 Unported License. Distribution or reproduction of this work in whole or in part requires full attribution of the original publication, including its DOI. [DOI: 10.1117/1.JMM.15.1.013510]

Keywords: extreme ultraviolet; patterned mask inspection; high-throughput electron microscope; projection electron microscope; multibeam scanning electron microscope.

Paper 15114P received Jul. 21, 2015; accepted for publication Mar. 8, 2016; published online Mar. 22, 2016.

#### 1 Introduction

Extreme ultraviolet lithography is a promising technique for post-2X nm generation lithography. Pattern inspection is one of the key issues that must be addressed in the fabrication of devices of half-pitch (hp) 16 nm and beyond. The inspection using deep ultraviolet (DUV) light, 199 and 193 nm in wavelength, had a throughput advantage, but continued shrinkage of pattern size has been leading toward difficulties in detecting small defects.<sup>1</sup> On the other hand, actinic (EUV) light, 13.5 nm in wavelength, is one of the candidates for EUV mask pattern inspection, but this has encountered many problems, such as source power, damage to mirrors and damage to masks, and high cost, which all remain to be addressed. An electron beam (EB) inspection system has an advantage of its high image resolution. The EB inspection system, however, does also have the disadvantage of its lower throughput, but that can be overcome by using a projectiontype microscope with wider illumination beam,<sup>2-4</sup> or by using multibeam scanning electron microscope (SEM) type inspection systems.<sup>5–8</sup>

In the pattern inspection system, the signal-to-noise ratio (SNR) is another critical parameter as it affects the defect detection sensitivity and minimizes false defect detection.<sup>9</sup> At higher illumination beam current densities, where a larger number of electrons per pixel is obtained on the detector, a higher SNR is achieved. However, high current densities of a focused illumination beam degrade the resolution due to blurring caused by the space charge effect.<sup>10</sup> The projection-type microscope is free from the space charge limit because of its wider illumination beam.<sup>2</sup> Moreover, any resolution degradation due to the aberration of imaging electron

optics (EO) can be improved by the aberration correction systems.<sup>11–14</sup> Therefore, we have been developing a projection electron microscope (PEM)<sup>2-3</sup> for pattern inspection, and have evaluated its feasibility.  $^{9,14-24}$  In order to accelerate this development program, the optimal inspection condition and the analysis of PEM image were investigated by using a computer simulation.<sup>9,18-23</sup> We had already reported that a ≥12-nm-sized defect on hp 64-nm mask pattern was detectable by the developed PEM,<sup>24</sup> and that the detectability is in good agreement with the simulation results.<sup>9,22,24</sup> It was also found that the optimal inspection conditions, such as landing energy, depend on defect size and shape.<sup>9,21,23</sup> In this paper, in order to investigate the nature of defect detectability of an EUV patterned mask using an EB inspection system, the impacts of defect size and shape, and of the line edge roughness (LER), the image processing algorithm on detectability are analyzed using the PEM and SEM inspection systems.

#### 2 Experimental

Figure 1 shows schematic illustrations of the PEM and SEM inspection systems. In the case of PEM, the beam covering a wide area illuminates the mask surface and generates secondary electrons (SEs) that are focused on a time delay and integration (TDI) sensor through the imaging EO.

The resolution of the PEM image is basically determined by the quality of the imaging EO, such as the point spread function, aberrations, and optical magnification, and also by the pixel size of the TDI sensor. In our currently developed PEM for the 16-nm node, the pixel size of the acquired image is set to  $16 \times 16$  nm. In previous work using our developed tool, hp 64- and 44-nm L/S patterns were successfully resolved and 12-nm-sized defects were detected without any false defect.<sup>15–16,24</sup> On the other hand, the resolution of the SEM image is basically determined by the spot size of the incident beam, and that is determined by the

<sup>\*</sup>Address all correspondence to: Susumu lida, E-mail: susumu.iida@eidec.co .jp



Fig. 1 Schematic illustrations of the (a) PEM and (b) SEM inspection system.

beam current and beam energy.<sup>10</sup> In order to analyze the defect detectability using PEM and SEM, simulated PEM and SEM images with hp 64-nm L/S patterned EUV masks were obtained using CHARIOT Monte Carlo software (Abeam Technologies Inc.).<sup>25</sup> The software with 72 cores was installed in an all-in-one server computer, Proliant DL 980 G2 (Hewlett-Packard) with 80 cores. In order to simulate the PEM image obtained by our developed tool, the pixel size and the number of electrons/pixel on the simulated PEM image were set to  $16 \times 16$  nm and 3000 electrons/ pixel, respectively. On the other hand, simulated SEM images were modeled on the multibeam SEM, developed by Malloy et al., and the current of a single beam was set to 672 pA.<sup>7</sup> The resolution of the simulated SEM image was determined by the pixel size of the image, which was varied from 1.5 to 6 nm. In order to obtain the same electron dose per unit area as that of the PEM, the dwell time was set to 6.1 ns (in the case of 1.5-nm-sized pixel). According to the ITRS-2014 update, the defect size on the EUV mask is defined as the square root of the defect area on a two-dimensional mask surface. Therefore, square-shaped programmed defects were used in the simulation to simplify the analysis of defect detection. In order to compare the PEM and SEM images with the same SE yields, a fixed landing energy of 1 keV was used to acquire these images. The detailed simulation conditions for PEM have been described in earlier reports.<sup>9,21–22</sup> The difference between the simulated image with defects and that without defects is defined as the difference image. In order to enhance the defect signal intensities, different types of image processing operations for PEM and SEM images were applied to the simulated images.<sup>9,17</sup> The EUV aerial image and the wafer printability of the mask defects were simulated using a LAIPH<sup>™</sup> defect printability simulator (Luminescent Technologies Inc.).<sup>26</sup> For a typical defect type, we focused on edge extrusion and edge intrusion, and we evaluated the printability with the printed space-width difference on a wafer. Illumination conditions were defined to match a state-of-the-art EUV pattern exposure system: numerical aperture = 0.33, sigma = 0.55 to 0.9, and illumination type as a dipole. Defect sizes that generated a 10% critical dimension error for 16-nm node patterns were then calculated. The LER of the L/S patterns was formed with normal random numbers and was described by a 3-sigma deviation of an edge from a straight line (3 sigma).

#### 3 Results and Discussion

#### **3.1** Comparison of Defect Detectability Between Electron-Beam and Deep Ultraviolet Inspection Systems

Figure 2 shows an EUV aerial image, EB (SEM) image, and EB (PEM) image of the hp 64-nm L/S patterned EUV mask. The contrasts of EB images are reversed compared with the aerial image. The contrast of the DUV image is similar to that of the aerial image because DUV is also reflected by the



Fig. 2 (a) EUV (aerial), (b) SEM, and (c) PEM images of hp 64-nm L/S patterned EUV mask.



Fig. 3 Schematic explanation of image contrast of EUV mask obtained by (a) DUV and (b) EB inspection tools.

EUV reflective multilayer (ML) and is also absorbed by the EUV absorber layer as shown in Fig. 3.

As a result, bright and dark areas in the DUV and EUV images directly correspond to the ML and absorber parts.<sup>1,27</sup> On the other hand, in the EB inspection system, the secondary electron emission coefficient (SEEC) determines the material contrast on the SE image.<sup>9,19,21,23</sup> The SEEC of the absorber is larger than that of the ML. Therefore, the bright and dark areas in the EB image correspond to the absorber and ML parts as shown in Fig. 2.<sup>9,19,21,23,27</sup> The defect image contrast is also influenced by the defect size and shape. In the cases of pinhole and intrusion defects, as the defects become smaller, the signals from the ML become lower in both cases of DUV and EB images. Hence, the contrast of those kinds of small defect signals becomes low in the case of DUV image,



Fig. 4 Experimentally obtained SEM image of a 30-nm-sized pinhole defect on hp 88-nm L/S pattern and its signal intensity profile (superimposed).



Fig. 5 Simulated printability of intrusion and extrusion defects on hp 64-nm L/S pattern.

but in the case of EB image, it becomes high as shown in Fig.  $4^{9,21,23}$ 

The intensity profile of a pinhole defect is much weaker than that of the ML level. This is because the SEs from the bottom of the pinhole are blocked by the side walls of the defect.<sup>9,21,23</sup> We had already reported that the PEM detectability of intrusion defects was better than that of extrusion defects as the defect size became small.<sup>9,21,23</sup> On the other hand, the printability does not coincide with the detectability in the case of EB inspection due to the reversed contrast.

Figure 5 shows simulated printability of the intrusion and extrusion defects on the hp 64-nm L/S pattern. The impact of an extrusion defect on printability is larger than that of an intrusion one.<sup>14</sup> Therefore, the inspection condition should focus on the detection of an extrusion defect due to its better printability. We had also reported that a landing energy with



Fig. 6 Schematic explanation of defect detection mechanism of PEM: (a) defect signal in base pattern and (b) defect signal intensity in difference image.



Fig. 7 Dependence of LER on (a) background noise of difference image and (b) defect detectability.

250 eV created the most sensitive condition to detect the 16nm-sized intrusion defect, whereas in the case of same size of an extrusion defect, 1000 eV created the most sensitive condition.<sup>9</sup> Because the detectability of an extrusion defect degrades under the condition of 250 eV, the landing energy should be set to 1000 eV to detect more printable defects by taking into account the printability.

#### **3.2** Impact of Line Edge Roughness on Defect Detectability of Projection Electron Microscope Inspection System

Figure 6 shows the defect detection mechanism of the PEM. The defect signal is determined by the summation of graylevel difference between defect and base pattern, similar to a DUV inspection system.<sup>14</sup> Defect signal intensity is defined as the ratio between the intensity peak and the standard deviation of the background noise in the difference image. Therefore, as the LER becomes large, the background noise of the base pattern becomes large in the difference image as shown in Fig. 7(a).

As a result, the defect signal intensity degrades with LER as shown in Fig. 7(b). However, this result also indicates that the detectability of extrusion and intrusion defects keeps its 10 sigma below 2 nm LER.

#### **3.3** Defect Detectability of Scanning Electron Microscope Inspection System

In order to investigate the nature of the pattern inspection with EB, the detectability of the SEM inspection system was evaluated. If the electron dose per unit area and the landing energy are the same, the gray level of the defect signal



Fig. 8 Simulated SEM image with 16-nm-sized defects on hp 64-nm L/S pattern and the difference images with image processing algorithm for SEM using binarization process before and after canceling the noises, with [(a1) through (a3)] 1 nm LER and [(b1) through (b3)] 2 nm LER, respectively. The resolution (pixel size of the image) is 1.5 nm, and the electron dose per unit area is the same as in PEM.

intensity in the difference image is the same between PEM and SEM when the same algorithm as PEM is used. The strong edge effect on an SEM image can enhance the defect signal intensity. However, the background noise derived from the LER is also enhanced. When the defect size is significantly larger than LER, a binarization process can take advantage of a higher resolution of the SEM. In this process, if the signal from a pixel is larger than a threshold, the absolute value of the signal intensity from the pixel becomes unity. On the other hand, if the signal is smaller than the threshold, the signal intensity becomes zero.

Figure 8 shows the simulated SEM images with resolutions of 1.5 nm, and the difference image using the image processing algorithm with the binarization process. As shown in Figs. 8(a2) and 8(b2), the binarized defect signals and the background noise in a difference image become 1 or -1. Pixel-sized signals, such as the background noise derived from the LER, are not printable defects. Moreover, because the signal area of the background noise is much smaller than that of the defect signal, the noise can be removed by a noise canceling process as shown in Figs. 8(a3) and 8(b3). This result shows that the high resolution leads to a robust detectability. However, in order to improve the throughput of the SEM inspection system, a lower resolution with a shorter dwell time is required. The resolution and dwell time correspond to the pixel size and number of electrons per pixel of the SEM image, respectively.

Figure 9 shows the dependence of the number of electrons per pixel on the detectability for the case of 6 nm resolution. A pixel size of 6 nm was the largest resolution to detect the 16-nm defect on the hp 64-nm L/S pattern with 1 nm LER. In the case with more than 100 electrons per pixel, all defects are detected. However, in the case of 50 electrons per pixel, pin-dot and intrusion defects are not detected, and a false defect is observed.

Figure 10 shows the standard deviation of the background noise in a difference image before the noise canceling process [as shown in Figs. 9(a1), 9(b1), and 9(c1)] as a function of the number of electrons per pixel. As the number of electrons becomes large, the background noise becomes low and shows asymptotic behavior. The asymptotic value is determined by the LER. If the LER is reduced to zero, the value approaches zero. This result shows that 100 electrons per pixel is a required condition for detection, but 200 electrons per pixel is a more robust condition. In the same manner, a pixel size of 3 nm was found to be the largest resolution to detect the 11-nm-sized defect on the hp 44-nm L/S pattern with 1 nm LER. Moreover, it was also found that 100 electrons per pixel is a required condition for detection, but 200 electrons per pixel is a more robust condition. The required pixel size to detect the defect can be approximated by the following equation:

$$p \le \frac{d}{2\sqrt{2}},\tag{1}$$

where p and d are the sizes of pixel and defect, respectively.

In real applications, there exist L/S patterns with LER aligned with 45 deg angle to a horizontal line as shown in Fig. 11. Moreover, when the center of a defect is located just on the corner of the pixels, this is the worst-case situation for defect detection. If at least one pixel is covered by the



**Fig. 9** Difference images with the resolution (pixel size of the image) of 6 nm before and after noise canceling process: number of electrons per pixel are [(a1) through (a2)] 200, [(b1) through (b2)] 100, and [(c1) through (c2)] 50, respectively. Sample target of the simulation is same as Fig. 8(a1) (1 nm LER).



Fig. 10 Standard deviation of difference image before noise canceling process [as shown in Figs. 9(a1), 9(b1), and 9(c1)] as a function of number of electrons per pixel.



**Fig. 11** Relationship between defect size and pixel size in the binarization process: (a) not detectable case and (b) detectable case. The L/S patterns are aligned with 45 deg angle to a horizontal line.

defect, the defect detection capability becomes more robust under the condition of 200 electrons per pixel.

However, if a part of the pixel is not covered by the defect as shown in Fig. 11(a), the probability that the signals from the pixel exceed the threshold level reduces. This leads to a degradation in the defect detection capability. Hence, in this situation, all of the nearest four pixels need to be covered by the defect as shown in Fig. 11(b). This condition is expressed as Eq. (1). According to the condition, the required pixel sizes for 16- and 11-nm-sized defects are estimated as ~6 and 4 nm; these values are almost in agreement with the simulation results of 6- and 3-nm-sized pixels, respectively. In the case of an 11-nm defect, the simulation result is smaller than the estimated value. This result shows that the impact of LER becomes large as the defect size becomes small. As a result, a smaller-sized pixel (higher resolution) is required to detect a smaller-sized defect. Malloy et al. have reported that the high-throughput image acquisition of EUV mask can be demonstrated using a multibeam SEM with 69 beams with currents of 672 pA in high-resolution mode.<sup>7</sup> By using this beam current, we can estimate the throughput to inspect the EUV mask with  $100 \times 100$ -mm-sized patterned area. The dwell time of 48 ns on a 6- and 3-nm pixel is needed to accumulate 200 electrons per these pixels. As a result, the throughput using a 69-beam SEM is found to be 58 and 232 h to scan the whole pattern area of EUV mask and to detect the 16- and 11-nm-sized defect on the hp 64-nm and hp-44 nm L/S pattern (16- and 11-nm node), respectively. On the other hand, in the PEM for the 16-nm node, because the TDI sensor has

Table 1 Comparison between multibeam SEM and PEM.

SEM	Target defect size (nm)	Pixel size (nm)	Number of beams	Throughput (h)
PEM	16	6	200	18.5
	11	3	1850	8.0
	Target defect size (nm)	Pixel size (nm)	Swath (mm)	Throughput (h)
	16	16	32.8	19 (600 MPPS)
	11	11	22.5	8 (>1.5 GPPS)

2048 pixels, a 32.8- $\mu$ m-wide image can be simultaneously acquired with a processing rate of 600 MPPS. Therefore, a throughput of 19 h can be achieved. However, in order to detect the 16-nm-sized defect on the hp 64-nm L/S pattern, 3000 electrons per pixel (16 nm) are required in PEM.<sup>9</sup> These results show that the required number of electrons per pixel for SEM to detect a same sized defect is an order of magnitude lower than that for a PEM due to the SEM's higher resolution.

Table 1 shows the performance comparison between PEM and multibeam SEM inspection system. In the PEM for the 11-nm node, which is now being developed, a throughput of 8 h with a pixel size of 11 nm is designed. In order to achieve the same throughput as PEM, 200 to 1850 beams are required in the multibeam SEM.

#### 4 Summary and Conclusions

Defect detectability using EB inspection for an EUV mask was investigated by comparing the PEM and SEM inspection systems. The detectability with EB does not concur with printability because the contrast of the EUV aerial image and the EB image for the EUV mask are reversed. Although the detectability of an intrusion defect is better than that of an extrusion defect in PEM, the impact of an extrusion defect on printability is larger than that of an intrusion one. Therefore, the inspection condition should focus on the detection of an extrusion defect to detect more printable defects. The detectability for a 16-nm-sized defect on the hp 64-nm L/S pattern was acceptable below 2 nm LER (3 sigma) in both PEM and SEM by applying a special algorithm for image processing. Pixel sizes of 6 and 3 nm were the lowest resolutions to detect the 16- and 11-nm defect on the hp 64-nm and 44nm L/S pattern with 1 nm LER for SEM inspection with an algorithm using the binarization and noise canceling processes, respectively. The accumulation of 100 electrons per pixel was a required condition for detection, but that of 200 electrons per pixel is found to be a more robust condition. This number was one order of magnitude lower than in the case of PEM due to the higher resolution for SEM. In order to achieve the same throughput as PEM, 250 to 1850 beams are required in the multibeam SEM.

#### Acknowledgments

The authors would like to thank Dr. Y. Midoh of Osaka University for his valuable advice for operating the simulator. This work was supported by New Energy and Industrial Technology Development Organization.

#### References

- 1. H. Hashimoto et al., "Development of a new mask pattern inspection tool NPI-7000, and applied results to EUV mask inspection," *Proc.* SPIE 8441, 844117 (2012).
  M. Miyoshi et al., "Electron beam inspection system based on the
- projection imaging electron microscope," J. Vac. Sci. Technol. B 19, 2852 (2001).
- 3. S. Yamaguchi et al., "Performance of EBeyeM for EUV mask inspection," Proc. SPIE 8166, 81662F (2011).
- M. Markos et al., "Electron optics for high throughput low energy electron microscopy," *Surf. Sci.* 601, 4733–4741 (2007).
- V. Marx, "Neurobiology: brain mapping in high resolution," Nature 5. K. Hull, Techology, Juli happing in high resolution, Fully 503, 147–152 (2013).
   A. Keller, D. Zeidler, and T. Kemen "High throughput data acquisition
- with a multi-beam SEM," Proc. SPIE 9236, 92360B (2014).
- 7. M. Malloy et al., "Massively parallel e-beam inspection: enabling nextgeneration patterned defect inspection for wafer and mask manufactur-Proc. SPIE 9423, 942319 (2015).
- M. Enyama et al., "Optical system for a multiple-beam scanning electron microscope," J. Vac. Sci. Technol. B 32, 051801 (2014).
- 9. S. Iida et al., "Extreme ultraviolet mask defect inspection with a half pitch 16-nm node using simulated projection electron microscope
- india and a sing similated projection electron heroscope images," *J. Micro/Nanolith. MEMS MOEMS* 12, 023013 (2013).
  T. Chang et al., "Multiple electron-beam lithography," *Microelectron. Eng.* 57–58, 117–135 (2001).
  R. Tromp et al., "A new aberration-corrected, energy filtered LEEM/
- PEEM instrument. I. Principles and design," Ultramicroscopy 110,
- 852–861 (2010). R. Tromp et al., "A new aberration-corrected, energy filtered LEEM/ PEEM instrument. II. Operation and results," *Ultramicroscopy* **127**, 12. 25-39 (2013).
- K. Grzelakowski, "A novel imaging energy filter for cathode lens electron microscopy," *Ultramicroscopy* 116, 95–105 (2012).
   R. Hirano et al., "Development of extreme ultraviolet mask pattern
- mispection technology using projection electron beam optics," *Micro/Nanolith. MEMS MOEMS* **12**, 021003 (2013).
- 15. R. Hirano et al., "Patterned mask inspection technology with projection electron microscope technique on extreme ultraviolet masks," J. Micro/ anolith. MEMS MOEMS 13, 013009 (2014).
- 16. M. Hatakeyama et al., "Development of inspection system for EUV mask with novel projection electron microscopy (PEM)," Proc. SPIE **8880**, 888028 (2013). 17. H. Watanabe et al., "EUV patterned mask inspection system using
- projection electron microscope technique," 88800U (2013). *Proc.* SPIE 8880,
- 18. S. Iida et al., "Identification of residual-type defect on extreme ultraviolet mask by projection electron microscope using Monte Carlo simula-
- tion," J. Vac. Sci. Technol. B **30**(6), 06F503 (2012). S. lida et al., "Impact of electron scattering in extreme ultraviolet reflec-tive multilayer on electron image," J. Vac. Sci. Technol. B **31**(6), 06F601 (2013).
- S. Iida et al., "Analysis of image distortion on projection electron micro-scope image," *Jpn. J. Appl. Phys.* 53, 116602 (2014).

- 21. S. Iida et al., "Impact of  $B_4C$  capping layer for extreme ultraviolet mask on the sensitivity of patterned mask inspection using a projection electron microscope," J. Micro/Nanolith. MEMS MOEMS 13, 043015 (2014).
- 22. S. Iida et al., "Simulation technique for pattern inspection using a projection electron microscope," J. Vac. Sci. Technol. B 33(6), 06FN02
- 23. S. Iida et al., "Pattern inspection of etched multilayer extreme ultraviolet mask," J. Micro/Nanolith. MEMS MOEMS 15, 021002 (2016).
- 24. R. Hirano et al., "Extreme ultraviolet lithography patterned mask defect detection performance evaluation toward 16- to 11-nm half-pitch generation," *J. Micro/Nanolith. MEMS MOEMS* 14, 033512 (2015).
  25. S. Babin et al., "CHARIOT: software tool for modeling SEM signal and
- e-beam lithography," *Phys. Procedia* **1**, 305–313 (2008). 26. C. Clifford et al., "Compensation methods using a new model for buried
- defects in extreme ultraviolet lithography masks," Proc. SPIE 7823, 78230V (2010).
- 27. A. Garetto et al., "AIMS EUV first light imaging performance," *Proc. SPIE* 9235, 92350N (2014).

Susumu lida received his BS and MS degrees in 1995 and 1997, respectively, and his PhD in electronics in 2000, all from Shizuoka University, Japan. He joined the Research and Development Center, Toshiba Corporation. In 2011, he was assigned to EIDEC, and since then he has been engaged in the development of patterned mask inspection.

Ryoichi Hirano received his BS and MS degrees in instrumentation engineering from Keio University in 1984 and 1986, respectively. He has joined Toshiba Corporation, where he carried out research on semiconductor equipment technology. In 2011, he was assigned to EIDEC, and since then, he has been engaged in the development of patterned mask inspection.

Tsuyoshi Amano received his BS and MS degrees in applied chemistry from Keio University in 1997 and 1999, respectively, and PhD degree in engineering from University of Hyogo in 2013. He joined Dai Nippon Printing Co. Ltd., where he carried out research on mask process, metrology, and repair technology. In 2011, he was assigned to EIDEC, and since then, he has been engaged in the development of patterned masks and blank inspection tools.

Hidehiro Watanabe received his BS and MS degrees in mineralogy from the University of Tokyo in 1983 and 1985, respectively. He has joined Toshiba Corporation, where he carried out his engineering duties in the area of photo mask. In 2011, he was assigned to EIDEC, and since then, he has been working as a General Manager of advanced mask research department.