High-sensitivity, wide-dynamic-range avalanche photodiode pixel design for large-scale imaging arrays

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Abstract. We present a detailed design study for a novel solidstate focal plane array of silicon avalanche photodiodes (APDs), with each detector in the array capable of operating with wide dynamic range in linear or Geiger-mode. The detector array is based on back-illuminated, crystallographically etched, (100) epitaxial silicon on R-plane sapphire substrates using 27 µm pixels, although other sizes could be supported as well. Analysis of the APD pixel design, which includes the photoelectron noise, dark current noise, and electronic circuit noise, shows the capability of imaging with a signal-to-noise ratio greater than 7 at -30 °C and ultralow illuminance of 10⁻⁴ lux. The detector array design will enable large focal planes, capable of imaging with high sensitivity and high speed, over a wide range of natural illumination conditions from direct sunlight to a cloudy moonless night. © 2010 SPIE and IS&T. [DOI: 10.1117/1.3316499]

1 Introduction

There is a growing need in science and industry for largescale detector arrays capable of imaging with high sensitivity and high speed near room temperature, over a wide range of natural illumination conditions from direct sunlight to a cloudy moonless night. Existing solid-state camera technology does not enable imaging in the visible spectrum over such a wide dynamic range near room temperature. In this design study, we demonstrate through detailed calculation means that a novel back-illuminated, wide dynamic range avalanche photodiode (APD) device design will enable imaging with high sensitivity near room temperature at -30 °C, over the full range of natural illumination conditions from the AM 0 in space orbit around Earth, to ultralow light levels when the illuminance at the camera lens $E_1 = 0.0001 \text{ lux [lumens/m²]}$, as on a cloudy moonless night when far from city lights. The high pixel sensitivity is achieved using a compact readout circuit, capable of controlling detector operation in linear and Geigermode regimes. The proposed wide dynamic range APD detector can operate in full depletion mode in the linear regime, when biased 8 V below the 36 V reverse breakdown voltage to support high quantum efficiency and speed. When charged to 44 V reverse bias, the detector design was shown in Ref. 1 to support Geiger-mode, and the efficiency of collecting photogenerated electrons to produce detection events was evaluated by Monte Carlo simulation to be above 80% for wavelengths between 300 and 800 nm.¹

The silicon APD array was designed with square pixels of 27 μ m side length for high resolution imaging with 100% sensitive-area-fill-factor, on a mechanically rugged and optically transparent sapphire substrate. When (100) silicon is properly etched with potassium hydroxide:isopropyl alcohol:water (KOH:IPA:H₂O) or tetramethyl ammonium hydroxide (TMAH) solution through a thermally grown oxide mask, square-based pyramidal frustum or mesa arrays result with the four mesa sidewalls of the APD formed by (111) silicon planes that intersect the (100) planes at a crystallographic angle ϕ_c =54.7 deg. The APD would be fabricated in the mesa using conventional silicon processing technology. The proposed APD design enables operation over a wide dynamic range in linear mode with or without internal gain for passive imaging in daylight conditions, and in single-photon sensitive Geiger-mode for active or passive imaging under low-light level conditions.^{1,2} The detector is back-illuminated through a light focusing spherical microlens fabricated in the thinned sapphire substrate that directs light toward the device interior and away from the silicon mesa sidewalls, thereby compensating the optical dead space between pixels resulting from the mesa isolation etch and also from the internal device guard diode geometry.

The sapphire microlenses can be coated with an antireflective $\lambda/4$ -MgF₂ layer, for example, to improve the optical power transmittance from air into the sapphire. In addi-

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Fig. 1 Advanced back-illuminated, crystallographically etched silicon-on-(*R*-plane) sapphire APD device for arrays showing a monolithically integrated sapphire microlens and $\lambda/4$ -MgF₂ and AIN antireflective layers.

tion, a lattice-matched (AlN) antireflective crystal layer can be grown at the sapphire-silicon interface to further increase the optical power transmittance into the device silicon, as shown in Fig. 1 and described in Ref. 2. The APDs in the array share a common, front-side anode contact, made locally at the base of each device mesa. A low resistance aluminum (Al) or copper (Cu) metal anode grid fills the space between pixels and also functions to inhibit optical crosstalk by shielding adjacent detectors from stray photons generated during the avalanche process.³ SOS-APD arrays are indium bump-bonded to complementary metal oxide semiconductor (CMOS) readout ICs to produce hybrid focal plane arrays (FPAs), as shown in Fig. 2, which might be 1024×1024 pixels or larger.⁴ The camera lens focal length and focal ratio maintain 100% sensitive-areafill-factor over the full FPA active area by limiting the maximum incidence angle of the extreme rays shown in red



Fig. 2 Back-illuminated, silicon-on-sapphire APD array chip, bumpbonded to CMOS readout IC. Extreme rays (shown in red) are incident at the maximum incidence angle θ_{1-max} . (Color online only.)

in Fig. 2, incident at the microlens edges of the pixel, located farthest from the camera optic axis at the corner of the FPA.²

The silicon APD is designed with a carefully optimized high electric field multiplication region, fabricated by high temperature diffusion of boron, described in Ref. 1. The boron impurity profile determines the spatial electric field in the device, and the boundary between the low electric field absorption-collection region and the high electric field multiplication region, as well as the APD guard diode geometry described by the guard diode boundary function $f_{GD}(x, y)$, are shown in Fig. 1. The narrow, high electric field multiplication region and the much wider low electric field collection-absorption region, help to provide stable, uniform gain with minimum variance, for nearly all of the photogenerated electrons in the device. Since most electron-hole pairs (EHPs) are generated through light absorption outside of the multiplication region, this results in a low excess noise factor for the APD.⁵⁻⁷

2 Device Characteristics of Wide Dynamic Range Avalanche Photodiode

The silicon-on-sapphire material system was originally developed for integrated circuit applications; however, it also has many ideal attributes for use as a substrate, enabling back-illuminated, solid-state, silicon detector arrays. Sapphire is an anisotropic, dielectric crystal of the negative uniaxial type that is weakly birefringent $(n_0 - n_e = 0.008)$ and possesses broadband optical transmittance ranging from the deep ultraviolet ($\hat{\lambda}_{o} = 200 \text{ nm}$) to the midwave IR $(\lambda_{o} = 5500 \text{ nm})$. Sapphire is extremely resilient, supporting thinning below 100 μ m, which is an important requirement for high resolution back-illuminated detector arrays. Sapphire can be optically polished to better than an 80 to 50 (scratch-dig) surface finish and can be etched optically using 157 nm excimer laser ultraviolet light to fabricate light focusing microlenses for the silicon detectors. Sapphire is chemically resistant to most liquid etchants at room temperature, and therefore functions as an ideal etchstop mateduring liquid crystallographic etching rial with (KOH:IPA:H₂O) or TMAH solution to define the silicon pixel mesa arrays. To enable high quantum efficiency backilluminated silicon detector arrays, the refractive index mismatch between air, sapphire, and silicon has to be corrected. The wide bandgap semiconductor material aluminum nitride (AlN) is closely lattice matched and refractive index matched to both sapphire and silicon, and offers the prospect of enabling fabrication of high transmittance (100) silicon-on-(AlN)-sapphire substrates for backilluminated silicon imagers.⁸⁻¹⁰

The APD in Fig. 1 has been designed for wide dynamic range operation by allowing the polysilicon cathode to extend beyond the edges of the mesa top over the thermally grown passivation oxide to form an MOS capacitor that acts as a charge-coupled device (CCD) gate. When the APD is reverse biased, the combination of the APD p/n-junction and the MOS capacitor formed by the overhanging polysilicon operates similarly to the input diode and input gate of a CCD.¹¹ When the wide dynamic range APD in Fig. 1 is reverse biased at voltages well below the reverse breakdown voltage of the detector, the p/n-junction

cannot deplete fully to the silicon-sapphire interface due to blocking by the high electric field boron impurity diffusion. However, the polysilicon MOS capacitor can deplete into the *p*-type silicon fully to collect any photogenerated electrons in the device. The APD p/n-junction prevents an inversion layer from forming under the polysilicon CCD gate by collecting the photoelectrons under the gate, producing an unmultiplied photocurrent. Therefore, at low reverse bias voltages, the polysilicon MOS capacitor depletes deep into the silicon to collect photogenerated electrons, which are in turn collected by the cathode of the APD p/n-junction, bypassing the high electric field region of the APD without being multiplied. At low reverse bias, the photogenerated electrons are mainly directed toward the MOS capacitor at the periphery of the device and relatively few photogenerated electrons in the APD reach the high electric field multiplication region. Therefore, the internal gain of the device remains low, even though the electric field in the multiplication region might reach the critical value of $\mathbf{E}_{\text{critical}} = 3 \times 10^5 \text{ V/cm}$ in silicon required for impact ionization. The region at the periphery of the device is indicated as a guard diode, because its purpose is to collect surface generated electrons. The electrons collected at the periphery will have reduced probability of drifting to the high field region to be multiplied. When the APD is biased above the reverse breakdown voltage, a much larger fraction of photogenerated electrons are directed toward the multiplication region of the device than at lower bias voltages, resulting in larger internal gain. For both linear and Geiger-mode operation, the device silicon will be fully depleted to enable efficient collection of photogenerated electrons from the entire silicon volume. These results were confirmed through extensive device simulations using MEDICI software of mesa APD structures created using the TSUPREM 4 fabrication simulation software as described in Refs. 1 and 2.

2.1 Calculated Photocurrent of Wide Dynamic Range Mesa Avalanche Photodiode

The sensitivity of the wide dynamic range APD in Fig. 1 can be evaluated by considering that the silicon detector images primarily over the visible wavelength range. Manufacturers of commercial consumer cameras that detect primarily in the visible spectral range often describe the camera sensitivity by specifying the minimum illuminance condition at the entrance aperture of the camera in photometric units of lux required to capture an image with clarity. Table 1 lists illuminance values for different imaging conditions.¹²

The natural illuminances (E_1) in Table 1 represent values at the entrance aperture of an imaging system of the type shown in Fig. 2. The range of illuminance values for direct sunlight listed in Table 1, existing at the camera lens aperture, are produced by solar spectral irradiance at airmass AM 0 and other airmass conditions arising for different angles of the sun above the Earth's horizon. These spectral irradiance curves can be used to calculate the total photon flux incident on the 27 μ m wide dynamic range mesa APD pixel that produces a photocurrent in response to the illumination. Under AM 0 illumination, when the wide dynamic range APD camera stares directly at the sun from Earth's orbit in space, the AM 0 spectral irradiance is given **Table 1** Natural illumination conditions and the corresponding illuminance E_i in photometric units at the entrance aperture or camera lens of the imaging system.

Illuminance (E_l)	Lux [lumens/m ²]
Direct sunlight at noon, very clear day	50,000 to 130,000
Daylight, sunny day	15,000
Daylight, cloudy	2,000
One hour before dusk	200
^a Dusk	10
Late dusk	1
Clear night, full moon	0.1
Clear night, quarter moon	0.03
Starlight, no moon	0.001
Cloudy, no moon, far from city lights	0.0001

^aLegal definition of dusk.

by the plot in Fig. 3.¹³ Figure 4 shows the geometry of imaging the sun directly using the APD-FPA camera with a lens focal length F=21 cm and focal ratio setting f/#=5.6, required for 100% sensitive-area-fill-factor over the full 1024×1024 FPA area, as calculated in Ref. 2.

The dashed blue curve in Fig. 3 represents the calculated spectral irradiance of the solar black body obtained from Eq. (1), which is derived from the Planck black body law.

$$F(\lambda)d\lambda = \frac{P(\lambda)}{4\pi r^2}d\lambda$$
$$= \frac{2\pi hc^2}{\lambda^5 [\exp(hc/\lambda k_b T) - 1]} \left(\frac{R_{sun}^2}{r^2}\right) d\lambda \ [W/m^2].$$
(1)

In Eq. (1), the average distance of the Earth from the sun is given by $r_{sun}=1.472 \times 10^{11}$ m and the radius of the sun $R_{sun}=6.96 \times 10^8$ m. The G173-03 standard described in



Fig. 3 Spectral irradiance of the AM 0 illumination based on the G173-03 ASTM standard (solid) and the calculated solar black body (dashed) with T=5800 K.



Fig. 4 Geometry of imaging the sun directly using the APD-FPA with camera focal length F=21 cm and f/#=5.6.

Fig. 3 is produced by the American Society for Testing and Materials (ASTM) for photovoltaic performance evaluation and also defines in addition to AM 0 (for orbit around the Earth), two standard terrestrial solar spectral irradiance curves both at AM 1.5 conditions.¹⁴

To calculate the AM 0 and AM 1.5 illuminances E_1 , the radiometric AM 0 and AM 1.5 spectral irradiances need to be converted to photometric spectral illuminances E_1 [lumens/m²-nm] using the conversion formula in Eq. (2).

$$P_{\text{lumens}} = K_{\text{max}} \int_{0}^{\infty} V(\lambda) p(\lambda) d\lambda.$$
⁽²⁾

The photopic response curve $V(\lambda)$ of the human eye is shown in Fig. 5 and Fig. 6 shows the spectral illuminance curves for solar AM 0 and AM 1.5 conditions. In Eq. (2), K_{max} =683 lumens/W and $p(\lambda)$ is the radiometric spectral irradiance.

Integrating the AM 0 spectral illuminance curve in Fig. 6 over wavelengths from 280 to 4000 nm yields a value of total illuminance that closely matches the maximum value of E_{1-AM} 0=130,000 lux in Table 1, as expected. Integrating the AM 1.5 Global Tilt and AM 1.5 Direct curves in Fig. 6 yields E_{1-AM} 1.5 GT \approx 110,000 lux and E_{1-AM} 1.5 D \approx 97,000 lux, respectively. These values represent illuminances in units of lumens/m² at the entrance aperture of a camera staring directly into the sun.

When imaging the sun directly, as shown in Fig. 4 using a 1024×1024 pixel focal plane array consisting of 27 μ m,



Fig. 5 Photopic response $V(\lambda)$ of the human eye. Peak response occurs at λ_o =555 nm.



Fig. 6 Spectral illuminance curves for AM 0, AM 1.5 Global Tilt, and AM 1.5 Direct solar conditions.

wide dynamic range mesa pixels shown in Fig. 1, the photon flux incident on a pixel and the resulting photocurrent produced by the pixel can be calculated. Table 2 lists important camera parameter values for calculating the photon flux incident on the 27 μ m wide dynamic range APD pixel when imaging the sun directly, as shown in Fig. 4.

Using the parameter values in Table 2, the optical power P_{opt} and photon flux Φ incident on a single APD pixel in the array can be calculated for different illuminance conditions at the camera lens aperture. If the camera is orbiting Earth in space and staring directly into the sun at the AM 0 condition, the irradiance at the camera lens will be $E_{\text{lens}}(\text{AM 0})=1356 \text{ W/m}^2=0.1356 \text{ W/cm}^2.^{13}$ The total optical power admitted by the lens is given by Eq. (3).

$$P_{\text{opt-total}} = E_{\text{lens}} A_{\text{aperture}}.$$
(3)

In Eq. (3), A_{aperture} is the camera aperture area. According to Eq. (3), the total optical power admitted by the lens is calculated to be $P_{\text{opt-total}}$ =1.4916 W. For the camera in Fig. 4,

Table 2 Parameters of the APD focal plane array camera.

Parameter	Value
Pixel size	27 µm
Pixel area A _{pixel}	$729 \times 10^{-8} \text{ cm}^2$
Focal plane array size	1024×1024
FPA side length	2.7648 cm
FPA area A _{FPA}	7.644 cm ²
Camera lens focal length	21 cm
Focal ratio setting f/ #	5.6
Camera entrance aperture area $A_{\rm aperture}$	11.04 cm ²
Area of the sun's image projected onto the FPA $A_{\rm sun-FPA}$	0.0309 cm ²
Total number of pixels that record the sun's projected image	4238 pixels

imaging the sun directly, the angle $\theta_{sun} = \arctan(R_{sun}/r_{sun}) = 0.0045$ rad and the corresponding solid angle subtended by the sun at the camera lens is given by $\Omega_{sun} = \pi(\theta_{sun})^2$ sr. The total optical power $P_{opt-total} = 1.4916$ W, projected by the sun into Ω_{lens} under AM 0 conditions will be focused onto the 4238 pixels on the FPA that record the sun's projected image. The total optical power incident per pixel is therefore given by $P_{opt-pixel} = P_{opt-total}/4238$ = 0.35 mW, however, not all of this incident optical power is actually detected by the mesa APD. As the sun descends toward the horizon, higher airmass conditions apply that produce less illuminance, approaching the 50,000 lux lower limit shown in Table 1.

Imaging a scene is different from direct imaging of the sun, because the scene is subtended by the field of view of the camera and the scene is projected onto the full light sensitive area of the FPA. Using the illuminance values at the camera aperture provided in Table 1, it is possible to calculate the total integrated photon flux and also the spectral photon flux curves with units [photons/sec-nm] as a function of wavelength that will be incident on a single 27 μ m mesa APD pixel shown in Fig. 1. The spectral photon flux density $\Phi(\lambda)$, incident on the 27 μ m mesa APD for the different scene illuminations, is used together with the previously calculated APD detector quantum efficiency $\eta(\lambda)$ from Ref. 2 to calculate the average APD photocurrent according to Eq. (4).¹⁵

$$\bar{i}_p = \eta G q \Phi. \tag{4}$$

The quantity η represents the APD detector quantum efficiency and depends on the optical wavelength, while G is the average internal avalanche gain of the APD, q is the charge of a single electron, and Φ represents the incident photon flux. In Fig. 7, the average unmultiplied (G=1) APD photocurrent i_p is calculated for different illumination conditions at the camera lens aperture for the 27 μ m mesa APD pixel in Fig. 1, without a microlens.

In Fig. 7, the APD photocurrent corresponding to a total detected photon flux of $\Phi_{GM}=10^7$ photons/sec represents the threshold photon flux for enabling Geiger-mode silicon APD operation near room temperature.¹⁶ If the mean linear mode photocurrent for the APD operating with unity gain is at or below $\langle i_{P-GM} \rangle = q \Phi_{GM} = 1.602$ pA, as shown in the figure, then Geiger-mode operation of the APD will be supported. This statement is based on the assumption that all photogenerated electrons would be multiplied in the APD to produce detection events or photon counts and also that dark count rates would be much lower than the photogenerated counts by three or more orders of magnitude to not saturate the count rate limit of the APD. The calculation in Fig. 7 shows that Geiger-mode operation of the 27 μ m mesa APD is possible only at night after late dusk.

The signal detected by the APD is calculated from the mean photocurrent in Fig. 7, according to Eq. (5).

$$S = \langle i_p \rangle^2. \tag{5}$$

To image with high sensitivity over the normal range of illumination conditions shown in Fig. 7, the APD optical receiver electronics should be capable of supporting linear and Geiger-mode operation.



Fig. 7 APD photocurrent as a function of the illuminance at the camera lens aperture for a 27 μ m mesa isolated pixel without a light focusing sapphire microlens, operating at unity internal gain *G*=1. Camera lens focal length *F*=21 cm with f/#=5.6.

2.2 Calculated Photocurrent Noise and Dark Current Noise of the Wide Dynamic Range Mesa Avalanche Photodiode

The photoelectron and dark current noise are contributed by the mesa APD detector to the overall noise of the pixel or optical receiver, that also includes a contribution from the electronic readout noise of the pixel control circuit. The photoelectron noise can be calculated for the two terminal mesa APD with guard diode shown in Fig. 1, with parameters β and γ describing the probability of a photogenerated electron being collected in the active part of the device and multiplied or collected by the guard diode and not multiplied, respectively. The parameters β and γ are therefore random variables. The instantaneous mesa APD photocurrent with a guard diode structure is given by Eq. (6).

$$i_{p}(t) = (1 - R)\{1 - \exp[-\alpha(\lambda)h(x, y)]\}\left(\beta + \frac{\gamma}{G}\right)Gq\Phi$$
$$= \frac{qG\beta}{T_{\text{int}}}m + \frac{q\gamma}{T_{\text{int}}}m.$$
(6)

Equation (6) expresses the instantaneous APD photocurrent where β , γ , and *G* are all random variables with assumed $\beta + \gamma = 1$. The quantity *R* represents the optical power reflectance, $\alpha(\lambda)$ is the absorption coefficient of silicon, and h(x, y) is the silicon mesa height. The quantity *m* represents the number of photogenerated electrons produced during integration time interval T_{int} and *G* describes the avalanche gain. The variables β and γ represent the fractions of *m* that undergo gain multiplication or not, respectively. The mean photocurrent and the variance of the photocurrent for the mesa APD with a guard diode can be calculated. The instantaneous photocurrent in the APD with guard diode is composed of multiplied and unmultiplied photoelectrons and both are expressed by Eqs. (7) and (8), respectively.¹⁵

$$i_a(t) = \sum_l X_l G_l \beta h(t - l\Delta t), \tag{7}$$

$$i_g(t) = \sum_l X_l G_l \frac{\gamma}{G_l} h(t - l\Delta t) = \sum_l X_l \gamma h(t - l\Delta t).$$
(8)

The total instantaneous photocurrent $i_p(t)$ can be written as the sum of Eqs. (7) and (8) as given by Eq. (9).

$$i_p(t) = \sum_l X_l G_l \beta h(t - l\Delta t) + \sum_l X_l \gamma h(t - l\Delta t).$$
(9)

The mean value of the random variable X_l is given by Eq. (10).

$$\langle X_l \rangle = p = T \eta_{abs} \Phi \Delta t. \tag{10}$$

In Eq. (10), *T* represents the optical power transmittance into the device and η_{abs} represents the absorption efficiency of light in the silicon. The expression for the mean photocurrent can therefore be written as shown in Eq. (11).

$$\langle i_p(t) \rangle = \sum_l \langle X_l \rangle \langle G_l \rangle \langle \beta \rangle h(t - l\Delta t) + \sum_l \langle X_l \rangle \langle \gamma \rangle h(t - l\Delta t)$$

= $p \langle G_l \rangle \langle \beta \rangle \sum_l h(t - l\Delta t) + p \langle \gamma \rangle \sum_l h(t - l\Delta t).$ (11)

Equation (11) can be solved in the limit of $\Delta t \rightarrow 0$ to yield the mean photocurrent in Eq. (12).

$$\langle i_p(t) \rangle = T \eta_{abs} \Phi \langle G_l \rangle \langle \beta \rangle \int_0^\infty h(t) dt + T \eta_{abs} \Phi \langle \gamma \rangle \int_0^\infty h(t) dt$$

= $T \eta_{abs} (\langle G_l \rangle \langle \beta \rangle + \langle \gamma \rangle) \Phi q.$ (12)

To calculate the variance of the photocurrent in the presence of random gain G, random β , and γ , the mean square value of the photocurrent must be calculated first according to Eq. (13) where the two currents are uncorrelated.

$$\langle i_p^2(t) \rangle = \langle i_a^2(t) + i_g^2(t) \rangle.$$
(13)

Calculating the mean square value of the photocurrent yields the result in Eq. (14).

$$\langle i_p^2(t) \rangle = T^2 \eta_{abs}^2 \Phi^2 \langle G \rangle^2 \langle \beta \rangle^2 q^2 + T \eta_{abs} \Phi \langle G^2 \rangle \langle \beta^2 \rangle (2q^2 B) + T^2 \eta_{abs}^2 \Phi^2 \langle \gamma \rangle^2 q^2 + T \eta_{abs} \Phi \langle \gamma^2 \rangle (2q^2 B) + T^2 \eta_{abs}^2 \Phi^2 \langle G \rangle \langle \beta \rangle \langle \gamma \rangle q^2 + T \eta_{abs} \Phi \langle G \rangle \langle \beta \gamma \rangle (2q^2 B).$$

$$(14)$$

The variance of the photocurrent in the APD device with a guard diode can now be calculated according to Eq. (15) using Eqs. (12) and (14).

$$\sigma_{i_p}^2 = \langle i_p^2(t) \rangle - \langle i_p(t) \rangle^2 = 2T \eta_{abs} \Phi q^2 B(F_G \langle G \rangle^2 F_\beta \langle \beta \rangle^2 + F_\gamma \langle \gamma \rangle^2 + F_{ag} \langle G \rangle \langle \beta \rangle \langle \gamma \rangle) - T^2 \eta_{abs}^2 q^2 \Phi^2 \langle G \rangle \langle \beta \rangle \langle \gamma \rangle.$$
(15)

Since G, β , and γ are random variables, a relationship between their mean square values and the square of the means can be defined using excess noise factors as formulated in Eq. (15).^{6,17} The excess noise factors for G, β , γ , and $\beta\gamma$ are provided in Eq. (16).

$$F_{G} = \frac{\langle G^{2} \rangle}{\langle G \rangle^{2}}; \quad F_{\beta} = \frac{\langle \beta^{2} \rangle}{\langle \beta \rangle^{2}}; \quad F_{\gamma} = \frac{\langle \gamma^{2} \rangle}{\langle \gamma \rangle^{2}}; \quad F_{ag} = \frac{\langle \beta \gamma \rangle}{\langle \beta \rangle \langle \gamma \rangle}.$$
(16)

Equation (15) describes the photoelectron noise for the mesa APD structure shown in Fig. 1, with two terminals, a guard diode region and random gain. In Eq. (15), the mean values for β and γ are calculated as a function of the optical wavelength for a particular guard diode boundary function $f_{GD}(x, y)$, seen in Fig. 1. The excess noise factors associated with β , γ , and $\beta\gamma$ can be determined experimentally in a fabricated device or by Monte Carlo simulation.¹⁸

In addition to the photoelectron noise contribution by the mesa APD device, the thermally generated dark current in the detector also contributes to the overall noise. The contribution to the detector dark current in the mesa APD structure of Fig. 1 with a guard diode will be composed of multiplied and unmultiplied dark current. The thermally generated dark current i_D in the mesa APD device can be defined and measured under low reverse bias conditions with the APD p/n-junction fully depleted to the silicon-(AlN)-sapphire interface, with a peak electric field below that needed for avalanche gain in the device.¹⁹ According to this definition, the mean APD dark current is given by Eq. (17).

$$\overline{i}_D = q\Phi_e = \frac{q}{T_{\text{int}}}\overline{m}_D.$$
(17)

In Eq. (17), the quantity Φ_e describes the average number of thermally generated electrons per second and q is the charge of an electron. The quantity \overline{m}_D describes the average number of dark electrons generated during integration time interval T_{int} . The thermally generated mean dark current $\langle i_D \rangle$ can be calculated to yield an estimate for the reverse biased silicon mesa APD p/n-junction. Assuming the dark current is only generated thermally in the silicon bulk, $p < n_i$ and $n < n_i$, where n_i is the silicon intrinsic carrier concentration and only a single type of trap with an energy located inside the bandgap participates; then the recombination rate U is described by Eq. (18).²⁰

$$U = -n_i \left[\frac{\sigma_p \sigma_n v_{TH} N_T}{\sigma_n \exp\left(\frac{E_T - E_i}{kT}\right) + \sigma_p \exp\left(\frac{E_i - E_T}{kT}\right)} \right].$$
 (18)

In Eq. (18), the quantities σ_n and σ_p represent the electron and hole capture cross section probabilities, v_{TH} is the carrier thermal velocity, N_T is the trap density, E_T is the trap energy level, E_i is the intrinsic Fermi level, and n_i is the intrinsic carrier density. Many of the quantities in Eq. (18) depend on temperature as does the recombination rate itself. The thermally generated dark current density from the silicon bulk is calculated by integrating Eq. (18) over the full width W of the reverse biased mesa APD p/n-junction depletion region, as given by Eq. (19).

$$J_{\text{gen}} = \int_{0}^{W} q |U| dx = q |U| W = \frac{q n_i W}{\tau_n}.$$
 (19)

In Eq. (19), the quantity τ_n represents the mean electron life time in *p*-type silicon material. An estimate of the mean

APD dark current from Eq. (19) is obtained by multiplying the result by the cross sectional area A_{APD} of the mesa APD device to yield Eq. (20).

$$\langle i_{D\text{-estimated}} \rangle = \frac{q n_i A_{\text{APD}} W}{\tau_n}; \quad \tau_n = \frac{1}{\sigma_n v_{TH} N_T}.$$
 (20)

The quantity τ_n in Eq. (20), depends on the electron capture cross section, carrier thermal velocity, and on the trap concentration. High values for τ_n in a silicon p/n-junction can reach 100 μ sec.^{21,22} Equation (20) assumes that the dark current arises from the silicon bulk and that surface generated dark current contribution to the total dark current is zero. This approximation is valid for well passivated mesa sidewalls. The formalism presented here describes the probabilities for dark electrons to be collected by the active mesa APD versus the guard diode and allows sufficient flexibility for the inclusion of the surface generated dark current contribution if needed.

When the reverse bias voltage on the APD is increased to the level of avalanche gain onset, a fraction of Φ_e will be multiplied and the instantaneous dark current in the APD will be described by the sum of multiplied dark electrons collected in the active silicon and unmultiplied dark electrons collected in the guard diode as given by Eq. (21).

$$i_D(t) = i_{Da}(t) + i_{Dg}(t),$$

$$i_{Da}(t) = \sum_l X_l \eta_{al} G_l h(t - l\Delta t),$$

$$i_{Dg}(t) = \sum_{l} X_l \eta_{gl} h(t - l\Delta t).$$
⁽²¹⁾

In Eq. (21), the quantities η_{al} , η_{gl} , and G_l are random variables with random values for each collection event. The quantity η_{al} describes the probability of a thermally generated electron being collected in the active region and therefore being multiplied in the APD, while η_{gl} describes the probability of a thermally generated electron being collected by the guard diode and therefore not being multiplied. The values G_l represent random variables of the gain imparted to a thermal electron generated during the time interval l.

The random variable X_l is described by Eq. (22).

$$X_l = \begin{cases} 0 \to 1-p\\ 1 \to p \end{cases}.$$
 (22)

The probability $p = \Phi_e \Delta t$ represents the probability of generation of a thermal electron during the time interval Δt . It is assumed as before that the variables $\{X_l\}$ are independent and the variables $\{G_l\}$ are also independent. The mean dark current can now be expressed using Eqs. (21) and (22) according to Eq. (23).

$$\begin{split} \langle i_D \rangle &= \sum_l \langle X_l \rangle \langle \eta_{al} \rangle \langle G_l \rangle h(t - l\Delta t) + \sum_l \langle X_l \rangle \langle \eta_{gl} \rangle h(t - l\Delta t) \\ &= \Phi_e \langle \eta_{al} \rangle \langle G_l \rangle \sum_l h(t - l\Delta t) \Delta t + \Phi_e \langle \eta_{gl} \rangle \sum_l h(t - l\Delta t) \Delta t. \end{split}$$

$$\end{split}$$

$$(23)$$

Taking the limit $\Delta t \rightarrow 0$, the sums in Eq. (23) become integrals according to Eq. (24).

$$\lim_{\Delta t \to 0} \sum_{l} h(t - l\Delta t) = \int_{0}^{\infty} h(t)dt = q.$$
(24)

The mean dark current can now be expressed according to Eq. (25).

$$\begin{split} \langle i_D \rangle &= \Phi_e \langle \eta_{al} \rangle \langle G_l \rangle \int_0^\infty h(t) dt + \Phi_e \langle \eta_{gl} \rangle \int_0^\infty h(t) dt \\ &= \Phi_e \langle \eta_{al} \rangle \langle G_l \rangle q + \Phi_e \langle \eta_{gl} \rangle q \\ &= \Phi_e q(\langle \eta_{al} \rangle \langle G_l \rangle + \langle \eta_{gl} \rangle). \end{split}$$
(25)

To calculate the dark current variance, the mean square dark current is calculated to yield the result in Eq. (26).

$$\langle i_D^2(t) \rangle = \Phi_e^2 \langle G \rangle^2 \langle \eta_a \rangle^2 q^2 + \Phi_e \langle G^2 \rangle \langle \eta_a^2 \rangle (2Bq^2) + \Phi_e^2 \langle \eta_g \rangle^2 q^2$$

$$+ \Phi_e \langle \eta_g^2 \rangle (2Bq^2) + \Phi_e^2 \langle G \rangle \langle \eta_a \rangle \langle \eta_g \rangle q^2$$

$$+ \Phi_e \langle G \rangle \langle \eta_a \eta_g \rangle (2Bq^2).$$

$$(26)$$

The variance of the dark current is given using Eqs. (25) and (26) according to Eq. (27).

$$\sigma_{i_D}^2 = 2\Phi_e q^2 B (F_G F_a \langle G \rangle^2 \langle \eta_a \rangle^2 + F_g \langle \eta_g \rangle^2 + F_{ag} \langle G \rangle \langle \eta_a \rangle \langle \eta_g \rangle) - \Phi_e^2 q^2 \langle \eta_a \rangle \langle \eta_g \rangle \langle G \rangle.$$
(27)

Since η_a and η_g are random variables, a relationship between their mean square values and the square of the means is defined using excess noise factors. The excess noise factors for G, η_a , η_g , and $\eta_a \eta_g$ are given in Eq. (28).

$$F_{G} = \frac{\langle G^{2} \rangle}{\langle G \rangle^{2}}; \quad F_{a} = \frac{\langle \eta_{a}^{2} \rangle}{\langle \eta_{a} \rangle^{2}}; \quad F_{g} = \frac{\langle \eta_{g}^{2} \rangle}{\langle \eta_{g} \rangle^{2}}; \quad F_{ag} = \frac{\langle \eta_{a} \eta_{g} \rangle}{\langle \eta_{a} \rangle \langle \eta_{g} \rangle}.$$
(28)

Equation (27) describes the dark current noise for the mesa APD pixel design in Fig. 1 with two terminals, a guard diode region and random gain. In Eq. (27), estimates for the mean values of η_a and η_g can be made by assuming that Φ_e has a contribution only from the thermally generated bulk dark current. The thermally generated electron flux Φ_e is calculated from Eqs. (20) and (17) and depends on the temperature. For the case of zero surface dark current, $\langle \eta_a \rangle$ represents the fractional volume of active silicon to the total device mesa silicon volume and $\langle \eta_g \rangle$ represents the fractional volume of the total mesa silicon volume. The pyramidal mesa frustum volume is given by Eq. (29).



Fig. 8 Analog readout circuit controls APD in the linear mode using the precharge method.

$$V_{\text{frustum}} = \frac{1}{3}h(A_1 + A_2 + \sqrt{A_1A_2}).$$
(29)

In Eq. (29), A_1 is the pyramidal frustum base area and A_2 is the pyramidal frustum top area. For the 27 μ m pixel mesa of height $h=10 \ \mu$ m, with a guard diode boundary defined by parameter $i_{GD}=1 \ \mu$ m with the same slope as the mesa sidewall as shown in Fig. 1, the mean fractional volume of the active and guard silicon is given by Eq. (30).

$$\langle \eta_a \rangle = \frac{V_{\text{active}}}{V_{\text{frustum}}} = 0.817,$$

 $\langle \eta_g \rangle = \frac{V_{\text{guard}}}{V_{\text{frustum}}} = 0.183.$ (30)

It is assumed for the calculation of the 27 μ m mesa APD pixel that the boundaries between the guard diode region and active region in the device are defined by the fixed boundary in Fig. 1, for which case the excess noise factors F_a and F_g in Eq. (28) are reduced to unity.

3 Pixel Circuit Design

The pixel sensitivity depends on the electronic readout circuit design which must be capable of controlling linear and Geiger-mode operation of the detector as shown in Fig. 7, to image successfully over the full range of natural illumination conditions. The pixel readout circuit in Fig. 8, based on a voltage source follower amplifier, may be used to control linear mode operation of the mesa APD and the circuit in Fig. 9 to control Geiger-mode operation.

The linear mode electronic readout circuit for the APD in Fig. 8 combines a voltage source follower amplifier consisting of transistor M1 and load resistor R_L , with pass transistor M3 to store the charge integrated at the gate capacitance of M1. The circuit is shown in the figure to be capable of performing correlated double sampling (CDS) using a second matching voltage source follower amplifier made up of transistor M2 with its own identical load resistor R_L and pass transistor M4. The readout circuit in Fig. 9 implements active quenching for the Geiger-mode APD to shorten the detector reset time. During a normal image acquisition cycle, the APD and readout circuit are first reset



Fig. 9 Digital readout circuit controls APD in the single photon sensitive Geiger-mode using active quenching.

by turning on then off reset transistor M2. Transistor M1 is then used to precharge the APD and the gate capacitance of M3 to the V_{bias} dc supply voltage. In response to just a single detected photon, the large avalanche gain in the APD $(G > 10^6)$ produces a photocurrent capable of quickly discharging the self-capacitance of the APD. The capacitance at the gate of M3 is also discharged by the APD photocurrent, thereby turning on M3. The transistor M3 senses the onset of the avalanche in the APD and turns on M4 which diverts a significant fraction of the charge to ground instead of it flowing through the APD junction where it can fill traps and lengthen the quench time of the detector following a detection event. An advantage of the circuits in Figs. 8 and 9 includes being able to combine them into a single compact pixel readout circuit that is capable of controlling both linear and Geiger-mode operation for the wide dynamic range mesa APD detector as shown in Fig. 10.

The circuit in Fig. 10 combines the analog APD readout circuit shown on the right with a digital readout circuit shown on the left. When the APD is biased in linear mode, transistor M7 is turned off and the APD is controlled by the analog circuit on the right. When the APD is biased in Geiger-mode, transistors M3 and M4 are turned off and M7 is turned on, allowing the APD to be controlled by the digital circuit on the left.



Fig. 10 APD readout circuit combines analog and digital readout circuits to enable linear and Geiger-mode APD detector operation for wide dynamic range imaging.



Fig. 11 APD pixel readout circuit is designed using the TSMC 0.25 μ m RF-CMOS process. M1 is biased into the constant current region after the linear capacitor C_{g-M1} is precharged. Analog APD readout circuit is shown without CDS.

3.1 Linear Mode Circuit Characteristics

During a normal image frame acquisition by the linear circuit, both pass transistors M3 and M4 are initially conducting or turned on. Transistor M6 resets the APD as indicated in the pixel control clocking waveforms. Transistor M5 is used to precharge the APD self-capacitance and the gate capacitance of M1 and M2 to the V_{CC} supply voltage. Transistor M4 is allowed to stay turned on briefly after M5 is turned off to allow the voltages at the gates of M1 and M2 to equalize. Following the voltage equalization, M4 is turned off, marking the start of the frame integration time $T_{\rm int}$, indicated in Fig. 8. During the integration time a photocurrent i_p , produced by incident illumination on the APD, discharges the precharged self-capacitance of the APD as well as the integration capacitance at the gate of transistor M1. At the end of the charge integration, pass transistor M3 is turned off and the charge is stored on the integration capacitor at the gate terminal of M1 which also serves as the input to the voltage source follower amplifier. Following the image frame integration time, the analog voltage output from the pixel circuit can be digitized using an A/D converter located on the periphery of the pixel array. The linear mode control circuit in Fig. 8 can provide low noise using CDS.

In contrast to the digital readout circuit that contributes zero electronic readout noise, the analog circuit composed of the voltage source follower and pass transistor with integration capacitor at the input of the voltage source follower, contributes to the electronic noise. Correlated double sampling shown in Fig. 10 can be used to reduce the noise contribution from the integration capacitor, however, such CDS circuitry may increase the area footprint required for the dual mode readout circuit in Fig. 10, beyond the $27 \times 27 \ \mu m$ limit. Therefore, it is more appropriate in the noise analysis to consider the readout circuit for linear mode operation without CDS. Such an analysis is also more conservative because it assumes the worst case noise contribution from the circuit. Figure 11 shows the linear mode readout circuit without CDS used in the signal-to-noise ratio analysis of the APD pixel.

The circuit in Fig. 11 is designed for implementation

Table 3 Linear mode electronic readout circuit parameters.

Parameter	Value
M1 gate length	<i>L</i> =0.5 μm
M1 gate width	W=1 μ m
Threshold voltage	V _{TN0} =0.45 V
Gate oxide thickness	t _{OX} =8 nm
Load resistor R _L	R_L =10,000 Ω
Integration capacitor C_{g-M1}	$C_{g-M1} \equiv C_{int} = 401.43 \ fF$

using the TSMC 0.25 μ m RF-CMOS process, and Table 3 lists important parameter values for the circuit.

In Table 3, the gate length of M1 is given as $L=0.5 \ \mu m$ because the foundry process offers a 5 V transistor with a thicker gate oxide and longer gate length. Using the higher voltage transistor in the pixel circuit allows biasing M1 into the active region when the gate capacitance is precharged at the start of signal integration. The RF-CMOS fabrication process supports a metal capacitor with capacitance per unit area of 1 fF/ μm^2 . Such a capacitor is linear and with a total area of 20 × 20 μm , provides 400 fF of capacitance at the gate of M1 is given by Eq. (31).

$$C_{g-M1} = \frac{2}{3}WLC_{OX} + 400 \text{ fF} = 1.43 \text{ fF} + 400 \text{ fF} = 401.43 \text{ fF}.$$
(31)

Transistor M1 of the voltage source follower in Fig. 11 can be biased into the active region with $V_{DS} > V_{GS} - V_{TN}$, when C_{g-M1} is precharged by M5 to $V_{IN} = VCC - V_{TN} \le 4.5$ V. The total charge stored on C_{g-M1} for $V_{IN} = 4.5$ V will therefore be given by Eq. (32).

$$Q_{C_g-M1} = C_{g-M1}V_{IN} = 1.8 \times 10^{-12} \text{ coulombs}$$

$$\rightarrow 1.13 \times 10^7 \text{ electrons.}$$
(32)

The input voltage $V_{\rm IN}$ at the gate of M1 is given by the initial charge Q_{C_g-M1} deposited on the integration capacitance C_{g-M1} corresponding to the precharge voltage V_{CC} , minus the charge that was sourced by the photocurrent flowing through the APD during the integration time interval given by Eq. (33).

$$V_{\rm IN} = V_{CC} - \frac{\langle i_p \rangle T_{\rm int}}{C_{g-\rm M1}}.$$
(33)

The voltage transfer characteristic of the source follower amplifier in Fig. 11 can be calculated by knowing that M1 operates in the active region. If the integration capacitor C_{g-M1} is precharged to a voltage $V_{IN}=4.5$ V as described in Eq. (32), the voltage transfer characteristic for the amplifier output voltage V_{OUT} as a function of V_{IN} can be calculated from Eq. (34) where V_{TN} is calculated iteratively taking into consideration the body effect.



Fig. 12 Source follower voltage amplifier linear output V_{OUT} as a function of input voltage V_{IN} .

$$V_{\rm OUT} = V_{\rm IN} - V_{TN} + \frac{1}{\left(\mu_n C_{ox} \frac{W}{L}\right) R_L} - \left\{ \frac{2(V_{\rm IN} - V_{TN})}{2\left[\left(\frac{1}{2}\mu_n C_{ox} \frac{W}{L}\right) R_L\right]} + \frac{1}{\left[2\left(\frac{1}{2}\mu_n C_{ox} \frac{W}{L}\right) R_L\right]^2} \right\}^{1/2}.$$
 (34)

The voltage transfer characteristic of the voltage source follower amplifier is plotted in Fig. 12.

3.2 Linear Mode Circuit Noise Characteristics

The electronic noise from the linear mode circuit in Fig. 11 is obtained by analyzing the small signal noise models.

The resistor r_{ds1} in Fig. 13 represents the finite output impedance of M1 including channel length modulation effects. The possible difference in potential between the source and body terminals of M1, gives rise to a voltagecontrolled current source with current given by $g_{s1}V_{s1}$. As a result, it can be modeled as a resistor with value equal to $1/g_{s1}$, as shown in Fig. 13. Since the three resistances R_L , r_{ds1} , and $1/g_{s1}$ are in parallel with $R_L ||r_{ds1}|| 1/g_{s1}$ and since $R_L \ll r_{ds1}$ and also $R_L \ll 1/g_{s1}$, then the noise due to the load resistor R_L will dominate as shown in Fig. 13. The noise from MOSFET M1 is also indicated in Fig. 13. The finite resistance of MOSFET channel M3 contributes to the noise of the sample and hold output shown in Fig. 14.

The noise voltage from the sample and hold circuit in Fig. 14 is contributed at the gate of MOSFET M1 and this can be converted into a noise current variance at the output of the source follower amplifier according to Eq. (35).²³



Fig. 13 Small signal noise model of the source follower amplifier of Fig. 11 showing noise current sources from a resistive MOSFET channel and from the load resistor R_L . Noise sources are in parallel and can be added to yield total output noise current.



Fig. 14 Sample and hold circuit of the source follower amplifier of Fig. 11 and the noise model.

$$\sigma_{i_{n-\text{sample and hold}}}^{2} = \frac{kT(g_{m1})^{2}}{C_{g-M1}}.$$
(35)

The total noise from the linear APD readout circuit of Fig. 11 is therefore expressed according to Eq. (36).

$$\sigma_{i_{en}}^{2} = \sigma_{i_{nFET}}^{2} + \sigma_{i_{nR}}^{2} + \sigma_{i_{nS \text{ and } H}}^{2}$$
$$= \frac{8}{3} kTBg_{m1} + 4kTB/R_{L} + \frac{kT(g_{m1})^{2}}{C_{g-M1}}.$$
(36)

The electronic noise result from Eq. (36) is used in the sensitivity analysis of Sec. 4 for the wide dynamic range mesa APD optical receiver based on the 27 μ m mesa APD of Fig. 1.

4 Sensitivity of the Linear and Geiger-Mode Avalanche Photodiode Optical Receiver

The sensitivity or signal-to-noise of the wide dynamic range mesa APD optical receiver is calculated using the mean photocurrent given in Eq. (4), together with the variance of the photocurrent in Eq. (15), the dark current in Eq. (27) and the electronic circuit readout noise in Eq. (36) according to Eq. (37).

$$SNR_{APD-linear-mode-receiver} = \frac{\langle i_p(t) \rangle^2}{\sigma_{i_p}^2 + \sigma_{i_D}^2 + \sigma_{i_{en}}^2}.$$
 (37)

Other sources of noise such as the background noise which is a type of photon noise associated with light from extraneous optical sources other than the signal of interest, is not considered in the analysis. Background noise is mainly a problem when imaging at midwave IR and longwave IR wavelengths where objects in a scene emit radiation according to the Planck black body law.¹⁷ Leakage currents are also not considered in the noise analysis because the surface leakage currents usually occur at poorly passivated surfaces of semiconductor p/n-junctions, for example, when ions on the semiconductor surface induce mirror charges in the semiconductor that form surface channels, enabling a surface leakage current to flow.²⁰ Bulk leakage currents may occur as a result of defects in the crystal structure of the bulk semiconductor material. In the mesa structure APD design presented here, the surfaces of the mesa are passivated with thermally grown silicon dioxide on the (100) and (111) planes. Therefore, it is expected that surface leakage currents at the APD p/n-junction will be minimized through good quality thermally grown oxide passivation. Surface leakage currents at the (111) mesa sidewalls of the APD are expected to be negligible compared to the bulk dark current in the device. The APD guard

Parameter	Value
APD focal plane array temperature	T=243 K (two-stage thermoelectric cooler)
APD array size	1024 $ imes$ 1024 pixels
APD pixel size	<i>a</i> =27 μm
APD mesa pixel height	<i>h</i> =10 μm
Microlens	No microlens
Camera lens focal length	<i>F</i> =21 cm
Camera focal ratio	f/#=5.6
Minority carrier lifetime τ_n in APD silicon	$\tau_n = 100 \ \mu sec^{22}$
APD ionization ratio	$k_{e,h}$ =0.2 used to calculate F_G after McIntyre
Transconductance of M1	$g_{\rm m1} {=} 0.85 { imes} 10^{-3} ~{ m A/V}$
Load resistor R_L	R_L =10 k Ω
Integration capacitor C_{g-M1}	$C_{g-M1} \equiv C_{int} = 401.43 \ fF$
APD integration time T_{int}	$T_{\rm int} = 1/2B$

Table 4 Sensitivity calculation parameters.

diode at the device periphery will also collect and limit the multiplication of any surface leakage currents.¹⁹

The signal-to-noise ratio is calculated for the linear mode 27 μ m mesa APD pixel according to Eq. (37) for the normal range of illumination conditions with the following assumptions listed in Table 4.

Using the parameter values listed in Table 4, the signalto-noise ratio for the wide dynamic range APD optical receiver in linear mode is calculated according to Eq. (37) for different natural illumination conditions. The results are shown in Figs. 15–21 for AM 0 to late dusk.

In Fig. 15 the APD stares into the sun and unity gain provides SNR above the unity threshold. The maximum integration time $T_{\text{int-max}}$ =17.6 ns is also indicated due to the large APD photocurrent that quickly discharges the integration capacitor of the linear readout circuit.

In Fig. 16 the maximum integration $T_{int-max}=25.7$ ns is indicated because of the large APD photocurrent that quickly discharges the integration capacitor of the linear readout circuit.

In Fig. 17, gain >3 becomes necessary to provide larger than unity signal-to-noise ratio.

In Fig. 18, gain >30 becomes necessary to provide larger than unity signal-to-noise ratio.

In Fig. 19, gain >200 becomes necessary to provide larger than unity signal-to-noise ratio.

In Fig. 20, gain >5000 becomes necessary to provide larger than unity signal-to-noise ratio.

In Fig. 21, gain >50,000 becomes necessary to provide larger than unity signal-to-noise ratio. At late dusk, the capability of the linear mode imager to provide high sensitiv-



Fig. 15 SNR of the linear mode APD imager under AM 0 (130,000 lux) illumination. Bandwidths of 1×10^4 Hz (thin solid line) and 1×10^9 Hz (dashed line) are shown. The minimum bandwidth is also shown (thick solid line).



Fig. 16 SNR of the linear mode APD imager under AM 1.5 (97,000 lux) illumination. Bandwidths of 1×10^4 Hz (thin solid line) and 1×10^9 Hz (dashed line) are shown. The minimum bandwidth is also shown (thick solid line).



Fig. 17 SNR of the linear mode APD imager on a sunny day with 15,000 lux illumination. Bandwidths of 1×10^4 Hz (thin solid line) and 1×10^9 Hz (dashed line) are shown.



Fig. 18 SNR of the linear mode APD imager on a cloudy day with 2000 lux illumination. Bandwidths of 1×10^4 Hz (thin solid line) and 1×10^9 Hz (dashed line) are shown.



Fig. 19 SNR of the linear mode APD imager before dusk with 200 lux illumination. Bandwidths of 1×10^4 Hz (thin solid line) and 1×10^9 Hz (dashed line) are shown.



Fig. 20 SNR of the linear mode APD imager at dusk with 10 lux illumination. Bandwidths of 1×10^4 Hz (thin solid line) and 1×10^9 Hz (dashed line) are shown.



Fig. 21 SNR of the linear mode APD imager at late dusk with 1 lux illumination. Bandwidths of 1×10^4 Hz (thin solid line) and 1×10^9 Hz (dashed line) are shown.



Fig. 22 SNR of the Geiger-mode APD imager at late dusk with 1 lux illumination. The Geiger-mode APD has 8-bit resolution and a quench time of 50 ns.



Fig. 23 SNR of the Geiger-mode APD imager on a clear night with full moon and 0.1 lux illumination. The Geiger-mode APD has 8-bit resolution and quench time of 50 ns.

ity for imaging becomes limited, especially at larger bandwidths. After late dusk, however, the Geiger-mode operation of the 27 μ m mesa APD detector without a microlens is supported as calculated in Fig. 7.

The signal-to-noise ratio for the Geiger-mode mesa APD optical receiver that uses the APD in Fig. 1 and the electronic readout circuit in Fig. 10 is calculated using the expression given by Eq. (38).

$$SNR_{APD-Geiger-mode-receiver} = \frac{\langle i_p(t) \rangle^2}{\sigma_{i_p}^2 + \sigma_{i_D}^2}.$$
(38)

In Eq. (38), the digital pixel control circuit does not add to the optical receiver noise.^{24,25} This enabling characteristic of the Geiger-mode pixel control circuit allows the optical receiver to operate with high sensitivity at the low light levels indicated in Fig. 7. Using the parameter values listed in Table 4, without the entries for the linear circuit, the signal-to-noise ratio for the wide dynamic range APD optical receiver in Geiger-mode is calculated according to Eq. (38). The results are shown in Figs. 22–26 for late dusk to a cloudy night with no moon.

In Fig. 22, the 27 μ m mesa APD pixel is shown to provide high sensitivity in the Geiger-mode. The SNR for $T_{\text{int}}=10 \ \mu$ sec and with 8-bit resolution is $\sim 1 \times 10^6$ with 50,000 frames/sec.



Fig. 24 SNR of the Geiger-mode APD imager on a clear night with quarter moon and 0.01 lux illumination. The Geiger-mode APD has 8-bit resolution and a quench time of 50 ns.



Fig. 25 SNR of the Geiger-mode APD imager in starlight with 0.001 lux illumination. The Geiger-mode APD has 8-bit resolution and a quench time of 50 ns.

In Fig. 23, the SNR for $T_{int}=200 \ \mu sec$ and with 8-bit resolution is $\sim 1 \times 10^5$ with 2500 frames/sec.

In Fig. 24, the SNR for $T_{int}=2.25$ msec and with 8-bit resolution is $\sim 1 \times 10^4$ with 220 frames/sec.

In Fig. 25, the SNR for $T_{int}=10$ msec and with 8-bit resolution is \sim 750 with 50 frames/sec.

In Fig. 26, the SNR for $T_{int}=10$ msec and with 8-bit resolution is \sim 7 with 50 frames/sec. The calculations of signal-to-noise ratio for the 27 μ m wide dynamic range mesa APD optical receiver operating in Geiger-mode show that the imager will be capable of recording an image even under the lowest natural illumination conditions in Fig. 26. This result is important because it confirms that the detector array design should be capable of imaging successfully under the broadest range of natural illuminations when cooled only to -30 °C using a compact two-stage thermoelectric cooler.

5 Concluding Remarks

It is shown in this detailed design study that a novel backilluminated silicon APD array architecture using (100) epitaxial silicon grown on R-plane sapphire having a latticematched AlN antireflective layer provides a unique means for creating high sensitivity, wide dynamic range APD device arrays for large area, high resolution, solid-state APD-FPA imagers with zero optical crosstalk and 100%



Fig. 26 SNR of the Geiger-mode APD imager on a cloudy night with no moon and 0.0001 lux illumination. The Geiger-mode APD has 8-bit resolution and a quench time of 50 ns.

sensitive-area-fill-factor. Such APD-FPA imagers could be used for both passive and active 2-D and 3-D imaging applications. The detailed calculations and analysis presented here show that the APD-FPA imager design will be capable of operating with high signal-to-noise ratio over the full range of natural illumination conditions near room temperature. Such a camera represents a novel, enabling technology for scientific and industrial applications.

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